

COMPOSITE DIELECTRIC FILTER DEVICE AND COMMUNICATION
APPARATUS INCORPORATING THE SAME

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to composite dielectric filters formed by arranging conductive films inside and outside dielectric blocks, and communication apparatuses incorporating the composite dielectric filters.

2. Description of the Related Art

The above-mentioned filters are used as band pass filters and the like in a microwave band. Particularly, in a single dielectric block, there are provided a duplexer formed by arranging a transmission filter passing signals through a transmission frequency band and inhibiting signals from passing through a reception frequency band and a reception filter passing signals through the reception frequency band and inhibiting signals from passing through the transmission frequency band. The duplexer is used as an antenna duplexer incorporated in an apparatus such as a mobile phone.

In terms of a composite dielectric filter device using a single dielectric block including such a plurality of filters, one important point in designing the device is to secure the isolation between the filters. For example, the

duplexer as the antenna duplexer is used to isolate transmission signals and reception signals. On the other hand, when the transmission signals are sent to a reception circuit, this has negative effects on the reception signals and thereby reception characteristics are deteriorated. As a result, the antenna duplexer can obtain characteristics capable of significantly attenuating transmission signals in the reception frequency band.

However, with the current trend toward the miniaturization of communication apparatuses, more compact composite dielectric filter devices have been manufactured. Consequently, there is a problem in that it is difficult to obtain isolation characteristics for measuring up to desired values.

SUMMARY OF THE INVENTION

Accordingly, it is an object of the present invention to provide a composite dielectric filter device capable of easily improving the isolation characteristics between adjacent filters even when using a compact dielectric block to miniaturize the entire configuration of the device. It is another object of the invention to provide a communication apparatus incorporating the composite dielectric filter device.

According to a first aspect of the invention, there is

provided a composite dielectric filter device including a parallelepiped rectangular dielectric block, a plurality of inner conductors extending in parallel from one face of the dielectric block to the opposite face, an outer conductor arranged on at least some of the outer faces of the dielectric block so that groups of adjacent inner conductors constitutes a plurality of filters, and a outer-conductor-free portion formed at a part of the outer conductor corresponding to the boundary between the mutually adjacent filters. With this arrangement, the coupling between the ground currents of the adjacent filters, that is, the inductive coupling between the ground current of one of the filters and the ground current of the remaining filter can be suppressed. As a result, the isolation characteristics between the mutually adjacent filters can be improved.

In addition, the outer-conductor-free portion may be formed around all the outer faces of the dielectric block. With this arrangement, since the coupling between the ground currents of the filters can be suppresses without fail, the isolation characteristics between the adjacent filters can be improved.

In addition, the composite dielectric filter device may further include input/output terminals extending from one of the outer faces of the dielectric block to another face thereof. The terminals may be isolated from the outer

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conductor and shared by the adjacent two filters and the outer-conductor-free portion. The outer-conductor-free portion may be continuous with the periphery of the terminal. With the outer-conductor-free portion continuous with the periphery of the input/output terminal, the effect of suppressing the coupling between the ground currents of the adjacent filters can be enhanced.

Furthermore, the composite dielectric filter device may further include ground-connected metal covers connected to the outer conductors of the dielectric block. The metal cover may be arranged independently for each of the outer conductors formed by separating at the outer-conductor-free portion. With the ground-connected metal cover independently arranged for each filter, the coupling between the ground currents of the adjacent filters can be suppressed.

According to a second aspect of the invention, there is provided a communication apparatus incorporating the composite dielectric filter of the invention. The filter is used as an antenna duplexer. This arrangement can sufficiently prevent a transmission signal from being input to a reception circuit and therefore satisfactory reception characteristics can be obtained.

BRIEF DESCRIPTION OF THE DRAWINGS

Figs. 1A to 1D illustrate the projections of a duplexer according to a first embodiment of the present invention.

Fig. 2 is an equivalent circuit diagram shown with consideration to the coupling of ground currents in the duplexer.

Fig. 3 is a graph showing changes in isolation characteristics depending on the presence and absence of an outer-conductor-free portion in the duplexer.

Figs. 4A to 4D illustrate the projections of a duplexer according to a second embodiment of the present invention.

Figs. 5A to 5D illustrate the projections of a duplexer according to a third embodiment of the present invention.

Figs. 6A and 6B show perspective views from above and below of a duplexer according to a fourth embodiment of the present invention.

Fig. 7 is a perspective view of a duplexer according to a fifth embodiment of the present invention.

Fig. 8 is a perspective view of a duplexer according to a sixth embodiment of the present invention.

Fig. 9 is a block diagram of a communication apparatus according to a seventh embodiment of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring to Figs. 1A to 1D, Fig. 2, and Fig. 3, a description will be given of the structure of a duplexer

according to a first embodiment of the present invention.

Figs. 1A to 1D illustrate the projections of respective faces of the duplexer. Fig. 1A shows a face of the duplexer, where the open-circuited-ends of inner conductors are formed. Fig. 1B shows a top view of the duplexer when mounted on a substrate. Fig. 1C shows a face of the duplexer, where the short-circuited ends of the inner conductors are formed. Fig. 1D shows a face of the duplexer to be mounted on a substrate. As shown in these figures, a substantially parallelepiped rectangular dielectric block 1 includes seven inner conductor holes 2a to 2g arranged in parallel extending from one face thereof to a face opposed to the face. On each of the inner surfaces of the holes 2a to 2g, an inner conductor 3 is formed. In each of the inner conductor holes 2a to 2c and the inner conductor holes 2e to 2g, the inner diameter of the hole on one open-face side is large and the inner diameter of the hole on the other open-face side is small to make the hole as a stepped hole. Near each of the open-faces having the large inner diameters, an inner conductor-free portion g is formed. The inner conductor-free portion g is used as an open-circuited end of each inner conductor.

An outer conductor 4 is formed on the six outer faces of the dielectric block 1. One end of the inner conductor formed on the inner surface of each of the inner conductor

holes 2a to 2c and 2e to 2g is short-circuited to the outer conductor 4 on a short-circuited face shown in Fig. 1C. On outer surfaces of the dielectric block 1, there are formed input/output terminals 6ant, 6tx, and 6rx isolated from the outer conductor 4.

The inner conductor hole 2d is a straight hole having a constant inner diameter. An inner conductor is formed on the inner surface of the hole 2d. The hole 2d is conducted to the outer conductor 4 on the face of the open-circuited end side shown in Fig. 1A. The other end of the hole 2d is conducted to the input/output terminal 6ant.

In addition, on the outer faces of the dielectric block 1, an outer-conductor-free portion 5 is formed at the boundary between a transmission filter formed by three resonators composed of the inner conductor holes 2a to 2c and a reception filter formed by three resonators composed of the inner conductor holes 2e to 2g. In the embodiment shown in Figs. 1A to 1D, from the face on the short-circuited-end side shown in Fig. 1C to the top face of Fig. 1B, and from the face on the open-circuited end side shown in Fig. 1A to the mounted face shown in Fig. 1D, the outer-conductor-free portion 5 is independently formed. The outer-conductor-free portion 5 can suppress the coupling between the ground currents of the mutually adjacent transmission and reception filters.

Fig. 3 shows how the isolation characteristics change depending on the presence or absence of the outer-conductor-free portion. In this graph, the lateral axis indicates frequencies and the vertical axis indicates the amount of transmission from a transmission-signal input terminal to a reception-signal output terminal. The broken line shows characteristics obtained when the outer-conductor-free portion 5 is not formed, and the solid line shows characteristics obtained when the outer-conductor-free portion 5 is formed. The boundary between a transmission frequency band and a reception frequency band is present at 1810 MHz. Hatched parts shown in the graph indicate attenuation necessary for the transmission filter at the reception frequency band and attenuation necessary for the reception filter at the transmission frequency band.

Fig. 3 shows how the isolation characteristics change depending on the presence or absence of the outer-conductor-free portion. In this graph, the lateral axis indicates frequencies and the vertical axis indicates the amount of transmission from a transmission-signal input terminal to a reception-signal output terminal. The broken line shows characteristics obtained when the outer-conductor-free portion 5 is not formed, and the solid line shows characteristics obtained when the outer-conductor-free portion 5 is formed. The boundary between a transmission frequency band and a reception frequency band is present at 1810 MHz. Hatched parts shown in the graph indicate attenuation necessary for the transmission filter at the reception frequency band and attenuation necessary for the reception filter at the transmission frequency band.

Thus, the formation of the outer-conductor-free portions permits the necessary characteristics to be obtained.

Next, referring to Figs. 4A to 4D, a description will be given of the structure of a duplexer according to a second embodiment of the present invention. Figs. 4A to 4D correspond to Figs. 1A to 1D used in the first embodiment. Fig. 4A shows the face of the duplexer where the open-circuited ends of inner conductors are disposed. Fig. 4B shows a top view of the duplexer mounted on a substrate. Fig. 4C shows the face of the duplexer where the short-circuited ends of the inner conductors are disposed. Fig. 4D shows the face of the duplexer to be mounted on the substrate. In the embodiment shown in Figs. 4A to 4D, an outer-conductor-free portion 5 is formed from the short-circuited-end face shown in Fig. 4C to the top face and to the mounted face. The outer-conductor-free portion 5 is continuous with the periphery of an input/output terminal 6ant, that is, with a part isolated from an outer conductor 4. The other arrangements are the same as those shown in Figs. 1A to 1D. As a result, since the continuous length of the outer-conductor-free portion 5 can be increased, the coupling between the ground currents of the transmission and reception filters can be effectively suppressed.

Next, referring to Figs. 5A to 5D, a description will

be given of the structure of a duplexer according to a third embodiment of the invention. Figs. 5A to 5D correspond to Figs. 1A to 1D shown in the first embodiment. Fig. 5A shows the face of the duplexer on which inner conductors are open-circuited. Fig. 5B shows a top view of the duplexer mounted on a substrate. Fig. 5C shows the face thereof on which the inner conductors are short-circuited. Fig. 5D shows the face of the duplexer to be mounted on the substrate. In the embodiment shown in figs. 5A to 5D, an outer-conductor-free portion 5 is disposed in a manner continuous with a part isolated from an outer conductor 4, that is, with the periphery of an input/output terminal 6ant, while continuously going around all the outer faces of a dielectric block 1, like a belt. The other arrangements in this embodiment are the same as those shown in Figs. 1A to 1D. In this manner, the coupling between the ground currents of a transmission filter and a reception filter can be more sufficiently suppressed, thereby improving the isolation characteristics between the filters.

Next, referring to Figs. 6A and 6B, a description will be given of the structure of a duplexer according to a fourth embodiment of the invention. Fig. 6A shows a perspective view of the dielectric duplexer seen from above. Fig. 6B shows a perspective view thereof seen from below. In each of the first to third embodiments, the inner

conductor for excitation is disposed between a transmission filter and a reception filter, inside the dielectric block. Then, the excitation inner conductor is coupled with the final-stage resonator of the transmission filter and the initial-stage resonator of the reception filter. However, in the fourth embodiment shown in Figs. 6A and 6B, with a transmission filter formed by three resonators composed of inner conductor holes 2a to 2c and a reception filter formed by three resonators composed of inner conductor holes 2e to 2g, there is provided an input/output terminal 6ant which is capacitively coupled with the inner conductor of the inner conductor hole 2c as the first-stage resonator of the transmission filter and is also coupled with the inner conductor of the inner conductor hole 2e as the initial-stage resonator of the reception filter. An input/output terminal 6tx is capacitively coupled with the inner conductor of the inner conductor hole 2a and an input/output terminal 6rx is capacitively coupled with the inner conductor of the inner conductor hole 2g. In the duplexer having such an arrangement, an outer-conductor-free portion 5 is disposed at the boundary between the transmission filter and the reception filter on some of the outer faces of a dielectric block. In this embodiment, on the top face of the dielectric block, the outer-conductor-free portion 5 is disposed in a manner continuous with the open-circuited-

end face of the inner conductors, and also, on the mounting face of the dielectric block, the outer-conductor-free portion 5 is disposed in a manner continuous with a part of the input/output terminal 6ant isolated from an outer conductor 4.

Next, referring to Fig. 7, a description will be given of the structure of a duplexer according to a fifth embodiment of the invention. In each of the first to fourth embodiments, the inner conductor holes having round sections are disposed and the inner conductors are formed on the inner surfaces of the holes. However, in the fifth embodiment shown in Fig. 7, inside a dielectric block, planer inner conductors 3a to 3c and 3e to 3g are formed to constitute stripline resonators. In this arrangement, similar to the previous embodiments, when an outer-conductor-free portion 5 is disposed at the boundary between a transmission filter and a reception filter on the outer faces of the dielectric block, the coupling between the ground currents of the filters can be suppressed, thereby increasing the isolation between the filters.

Next, referring to Fig. 8, a description will be given of the structure of a duplexer according to a sixth embodiment of the invention.

Fig. 8 shows a perspective view of the duplexer mounted on a substrate. In this figure, reference numerals 7rx and

7tx denote metal covers covering the open face of a dielectric block and electrically connecting outer conductors 4rx and 4tx formed on the outer surface of the dielectric block to a ground electrode on the mounted substrate. The conductor 4rx denotes the outer conductor of a reception filter side and the conductor 4tx denotes the outer conductor of a transmission filter side. The structure of the dielectric block is the same as the structure shown in Figs. 6A and 6B.

In this manner, the outer conductors 4rx and 4tx formed by separating at the outer-conductor-free portion 5 are grounded via the mutually independent metal covers 7rx and 7tx. Due to ground currents flowing through the metal covers 7rx and 7tx, the coupling between the ground currents of the transmission filter and the reception filter can be suppressed, thereby increasing the isolation between the filters.

Next, referring to Fig. 9, a description will be given of a communication apparatus according to a seventh embodiment of the invention.

In Fig. 9, there are shown a transmission/reception antenna ANT, a duplexer DPX, band pass filters BPFa and BPFb, amplifying circuits AMPa and AMPb, mixers MIXa and MIXb, an oscillator OSC, and a frequency synthesizer SYN.

The mixer MIXa mixes an IF signal of a transmission

signal with a signal output from the SYN. Of the mixed signals output from the mixer MIXa, the band pass filter BPFa passes only the signals of a transmission frequency band. The amplifier circuit AMPa power-amplifies the signals to transmit from the antenna ANT via the duplexer DPX. The amplifier circuit AMPb amplifies a reception signal extracted from the duplexer DPX. Of the reception signals output from the amplifier circuit AMPb, the band pass filter BPFb passes only the signals of a reception frequency band. The MIXb mixes a frequency signal output from the SYN with the reception signal to output an intermediate frequency signal IF of the reception signal.

The above duplexer DPX is the duplexer having the structure shown in one of Figs. 1A to 1D and Figs. 4A to Figs. 8.

In each of the above embodiments, in order to couple the resonators composed of the inner conductors disposed inside the dielectric block, the inner conductor holes have the stepped configuration and the open-circuited ends of the holes have top-end capacitances formed of inner conductor-free portions. Besides, there are other applicable methods. For example, on the open face of a dielectric block, electrodes for coupling resonators are formed extending from inner conductors to the openings of adjacent inner conductors so that the coupling between the mutually

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adjacent resonators can be made. Alternatively, holes, cavities, or slits used for coupling are formed between the adjacent inner conductor holes to couple the adjacent resonators. Any of these methods can be similarly applied to the present invention.

As described above, the outer-conductor-free portion is formed at a part of the outer conductor corresponding to the boundary between adjacent filters. With this arrangement, the coupling between the ground currents of mutually adjacent filters can be suppressed and thereby the isolation characteristics between the adjacent filters can be improved.

In addition, the outer-conductor-free portion is formed around all the outer faces of the dielectric block. This arrangement can suppress the coupling between the ground currents of the filters without fail and thereby the isolation characteristics between the mutually adjacent filters can be improved.

In addition, the outer-conductor-free portion formed on the outer face of the dielectric block is arranged continuously with the periphery of the input/output terminal shared by the mutually adjacent two filters. Thus, since the outer-conductor-free portion is continuously extended to the periphery of the input/output terminal, the coupling between the ground currents of the mutually adjacent filters can be effectively suppressed.

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In addition, ground-connected metal covers continuous with the outer conductor of the dielectric block are independently arranged for the respective outer conductors formed by separating at the outer-conductor-free portion. That is, the ground-connected metal covers are independent for the respective filters. Thus, the coupling between the ground currents of the mutually adjacent filters can be effectively suppressed.

Furthermore, according to this invention, the composite dielectric filter device having the above-described structure is incorporated in an antenna duplexer or the like to constitute a communication apparatus. As a result, since the arrangement can prevent a transmission signal from being sent to a reception circuit, satisfactory reception characteristics can be obtained.

While embodiments of the present invention have been described above, variations thereto will occur to those skilled in the art within the scope of the present inventive concepts, which are delineated by the following claims.